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Senior RF/MMW IC Designer

Leuven, Belgium

Our client is a startup headquartered in the dynamic city of Leuven, Belgium. It designs leading-edge chip and antenna technology for next-generation wireless systems. We look for talented and enthusiastic new members to join the quickly growing multinational team. The company offers a flexible, agile and challenging work environment and an attractive compensation package.

Description

We are looking for a senior RFIC designer who owns and contributes to several parts of the RFIC, including RF front-end, wideband analog and PLL circuits, overseeing layout work and take technical lead of small teams as required.

Responsibilities include:

- design of analog and/or RF blocks (e.g., PA, LNA, mixer, divider, multiplier, VCO, PLL);
- design of mm-wave passive structures (e.g., TL, transformer, power splitter/combiner);
- design of GHz-range analog baseband amplification, filtering and buffering circuits;
- implementation of ESD, DFT, DC, biasing and calibration blocks and methods;
- Top-level chip design and system simulation.

Job requirements

You have a M.Sc. or Ph.D. in electrical engineering, and you have a solid expertise in analog, RF and/or mm-wave IC design with a silicon-proven industry track record. You worked with advanced CMOS technology nodes and you worked for high-speed wireless communication applications. You are eager to join a dynamic team developing products for next-generation 5G systems. You are a self-motivated, independent and efficient team member that has good communication skills and brings a lot of IC design industrial experience to the team.

The successful candidate has a proven industry track record with broad expertise in analog, RF and mm-wave IC design.

Following requirements are a must, candidates must tick all boxes to be considered:

- degree in Electrical Engineering with at least 10 years of industrial experience in analog/RF/MMW IC design;
- solid background in GHz analog, RF and/or mm-wave design;
- DFT and PVT-aware design experience including biasing blocks (e.g., Bandgap, PTAT, LDO);
- contribution to several successful product chips with analog/RF/MMW circuits;
- advanced user of Cadence Virtuoso custom IC tool suite;
- Passive design experience (e.g., TL, transformer, power splitter/combiner);
- experience with ADS, momentum, EMX, HFSS.

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Following qualifications will make you stand out:

- experience with ESD design and strategy, and with chip finishing (e.g., IO ring design);
- experience with mixed-signal top-level simulation and verification;
- knowledge of PDK development and skill coding;
- experience with characterization and ATE data review and correlation to simulations;
- experience with package modeling and interactions with packaging engineer.

For more information please contact Mr. Anton van Rossum, Recruitment Consultant,
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