

RFIC Antenna Designer

Peraso Technologies

Peraso is a fabless semiconductor company specializing in the development of chip sets for the 60 GHz marketplace. Peraso's goal is to provide best in class products for the consumer electronics (CE) market in general, and specifically to the mobile segment, including smartphones, tablets, and digital cameras. As such, Peraso endeavors to incorporate features critical to the mobile market in our products, including minimal footprint, low power consumption and competitive price points.

Description and Responsibilities

We are actively seeking an RFIC Antenna Design Engineer for mm-wave applications (60 GHz). The successful candidate is highly organized and possesses recent experience in RF product and system design and testing, ideally in a semiconductor product company.

Additional responsibilities include:

- Design complex antenna solutions for portable electronic application, including dual polarized antennas, circularly polarized and phased array antennas
- Drive the development of the technical documentation for our RF products including datasheets, application notes and user guides.
- Perform schematic capture and layout using Cadence Design Entry and PCB for reference designs as required.
- Supplier communications and technology team support in relation to new semiconductor packaging applications.
- Support and develop current and future cost effective antenna and packaging technologies for Peraso's mixed-signal/RF radio ICs, such as LTCC, substrate technology and PCB
- Work closely with our early access partners in the development of new products and; this may require occasional travel to the U.S., Europe and Asia.

Requirements

M.Sc. in Electrical Engineering or related field and at least 10 years of relevant industry experience. PhD highly desired.

Experience in multilayer IC layout design, semiconductor or semiconductor packaging preferably using Cadence SiP (System in Package) or Cadence APD (Allegro Package Design), or Allegro PCB Designer.

Experienced with 3D antenna design tools such as HFSS

Experienced with waveguide and microwave component design tools

Experienced with phased array technology

Knowledge of PCB and substrate materials and fabrication processes; proficient in geometric dimensioning and tolerance. Proficient with AutoCAD, Solidworks and/or similar applications is a plus.

Hands-on experience with RF package design, simulation and model extraction including in-package antenna design *is a plus*

A self-starter with high energy with the ability to work independently and take on tasks outside area of expertise.

Careers at Peraso

Peraso offers engineers a chance to participate in some of the most advanced technology development in the world. Specialties include 60 GHz circuit design, advanced antenna and packaging design, high speed mixed signal and digital design, PHY/MAC architecture development and real-time software development. Peraso is practicing true state-of-the-art technology development, and is committed to nurturing a best-in-class engineering organization.

Your contribution to success in our fast paced, expanding organization will be rewarded with a competitive compensation package that includes a generous stock option plan. If you're ready to be a pioneer in the emerging mmwave market, contact us at: jobs@perasotech.com.