

RFIC Design Manager

Leuven, Belgium

Summary

We are looking for a hands-on RFIC Design Manager to lead our world-class mmWave chip design team and make key contributions to our challenging IC designs. We need solid expertise in analog, RF and/or mm-wave IC design with a silicon-proven industry track record on advanced CMOS technology nodes, preferably for high-speed wireless communication applications.

Responsibilities

You are a seasoned industry professional who will:

- Lead and grow our IC design team;
- Contribute to the design of analog and/or RF blocks, mm-wave passive structures, GHz-range analog baseband amplification, filtering and buffering circuits;
- Contribute to chip design project plans, budgeting, execution and reporting;
- Plan and implement top-level chip design and system simulation, ESD, DFT, DC, biasing and calibration blocks and methods.

Requirements

You have a proven industry track record with broad expertise in analog, RF and mm-wave IC design. You are eager to join and lead a dynamic team developing products for next-generation 5G systems. You are a self-motivated, independent and efficient team member that has good communication skills and brings a lot of IC design industrial experience to the team.

Following requirements are a must:

- At least 15 years of industrial experience in analog and/or RF/MMW IC design including contribution to several successful product chips;
- Proven team lead capabilities including hiring, coaching, evaluating and improving your team members' overall performance;
- advanced user of Cadence Virtuoso custom IC tool suite.

Following qualifications will make you stand out:

- experience with ESD design and strategy, and with chip finishing (e.g., IO ring design);
- experience with mixed-signal top-level simulation and verification;
- experience with characterization and ATE data review and correlation to simulations.

For more information please contact Mr. Anton van Rossum, Recruitment Consultant,
M: +31 6 53 306 309, E-Mail: anton.van.rossum@ir-search.nl.