

PIDSO is an Austrian high-tech company based in Vienna's 23rd district, which specializes in the development and manufacture of antennas and antenna systems. We are always on the lookout for new high potentials who work together with us to advance our products and inspire our customers. We are currently looking for a dedicated and motivated to immediate entry

Hardware Developer (m / f) full time

Your profile:

- University degree TU / FH / HTL - Electronics, Electrical Engineering Telecommunications
- Experience in the automotive industry is an advantage
- Requirements Engineering
- Min. 3 years experience in analog and digital circuit design
- Experience in mixed-signal measurements
- Experience in RF circuit and board design
- High-speed board design
- Altium Designer
- Experience in Keysight ADS is an advantage
- Independent and self-responsible way of working
- Teamplayer, motivated, reliable
- High degree of accuracy and responsibility

Your tasks:

- Design of schematics and PCB layout (Analog / Digital / HF Design)
- Multi-layer, high-speed, RF board design
- Circuit simulation
- Contact with PCB manufacturer and mounter
- Construction and metrological evaluation of PCB prototypes
- Documentation, creation of logs

We offer:

- Flat hierarchy and a nice, motivated team with a very good working atmosphere
- Flexible working hours, a break room with coffee, tea and snacks at leisure
- A modern workplace, free parking and good public transport
- Regular team events
- Varied continuing education and development opportunities
- A long-term position with a varied range of responsibilities

The gross monthly salary is EUR 3,200. Of course, an overpayment with appropriate qualifications and experience is possible. Could we spark your interest? Would you like to join the PIDSO team? Then we look forward to meeting you! Apply now with your meaningful application documents, Curriculum vitae including photo and motivation letter at office@pidso.com